

PATENT 0941-0809P

THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

LAI, Sheng-Chih et al. Conf.:

Appl. No.:

10/642,244

Group:

2823

Filed:

August 18, 2003

Examiner: W. D. Coleman

For:

MASK READ ONLY MEMORY CONTAINING DIODES AND METHOD OF MANUFACTURING THE SAME

LARGE ENTITY TRANSMITTAL FORM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 29, 2004

Sir:

Transmitted herewith is a Reply to Restriction/Election

Requirement in the above-identified application.	
	The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
	Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). $$0.00$ for the extension of time.
\boxtimes	No fee is required.
	A check in the amount of \$0.00 is enclosed.
	Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this sheet is attached

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §\$1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

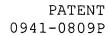
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KM/asc 0941-0809P

Attachment(s)





IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: LAI, Sheng-Chih et al. Conf.: 8162

Appl. No.: 10/642,244 Group: 2823

Filed: August 18, 2003 Examiner: W. D. Coleman

For: MASK READ ONLY MEMORY CONTAINING DIODES

AND METHOD OF MANUFACTURING THE SAME

REPLY TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 October 29, 2004

Sir:

Responsive to the Office Action dated September 29, 2004, the following election and remarks are respectfully submitted in connection with the above-identified application.

REMARKS

Claims 1-19 are now pending in the present application.

The Examiner has given the following Restriction Requirement:

Group I, claims 1-11, drawn to a semiconductor device; and

Group II, claims 12-19, drawn to a method of manufacturing a semiconductor device.

In response to this requirement, Applicants hereby elect Group I, claims 1-11 drawn to a semiconductor device. This election is with traverse.